

843.41042X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): K. Hayano, et al.

Application No.: 10/025,457

Filing Date: December 26, 2001

For: METHOD OF MANUFACTURING PHOTOMASK AND METHOD OF
MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

Art Unit: 1756 ✓

Examiner: S. Mohamedulla

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RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 29, 2003

Sir:

In response to the Office Action mailed September 29, 2003, Applicants respectfully elect the Group II claims (that is, claims 17-32, drawn to a method of making a semiconductor device). This election is made without traverse.

In view of the foregoing, examination of the Group II claims, claims 17-32, on the merits, in the above-identified application, in due course, is respectfully requested.

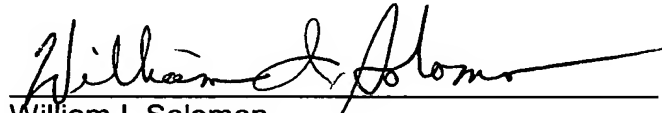
To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing of

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this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 843.41042X00) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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